Reviewed by:Andrew Newman

Module Name:Acceptance Filter

Jira Ticket:[CAN2-27]

**CAN Controller Requirements Checklist Rev.0**

1. Is requirements document written clearly and grammatically correct?

Yes [ ] No [x]

Comments:ACCEPT\_INIT\_01 is a complex requirement. Advise splitting it into multiple reqs. Also o\_rx\_fifo\_w\_data[127:0] is not set a value.

1. Does requirements document define all functions to be performed by the module?

Yes [ ] No [x]

Comments: ACCEPT\_MASKMSG\_01 - ACCEPT\_MASKMSG\_04 are supposed to be daisy chained( i.e. in \_02, input is the contents of internal register 1, also give them a name and a size for clarity)

MASKID functionality is described in an invalid manner.

1. Does requirements document include all inputs and outputs to the module along with their corresponding accuracy, range of values, frequency and format?

Yes [ ] No [x]

Comments: The ID range in the i\_rx\_message is [127:96] instead of [31:0].

1. Is each requirement unique?

Yes [x] No [ ]

Comments:

1. Does requirements document comply with system requirements and traceability?

Yes [x] No [ ]

Comments:

1. Is terminology consistent with Requirements Standards?

Yes [ ] No [x]

Comments: No comments allowed in requirement body itself, add notes column.

Complex requirements are not allowed, i.e. change only one thing at the time.

Every requirement should begin with “The module shall”. Refer to requirement standards for this and more.

1. Is it possible to implement all requirements?

Yes [ ] No [x]

Comments: ACCEPT\_TRANSMIT\_04 does not define the parameters of acceptance or rejection. Split it into FSM state where it tracks like IDLE, BUSY, ACCEPT, REJECT; and work off of these stages.

1. Is each requirement testable or verifiable?

Yes [ ] No [x]

Comments: See 7.